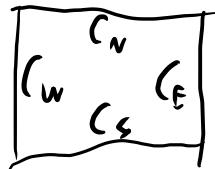


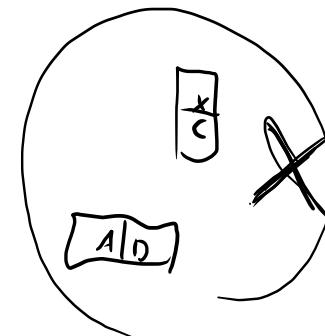
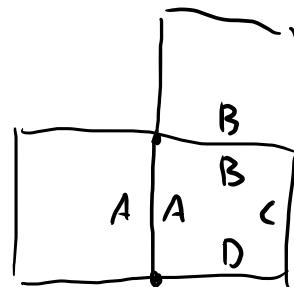
Definitions:

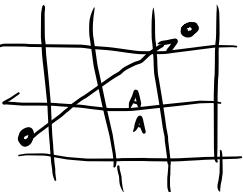
Wang tile over color set Σ : $(c_E, c_N, c_W, c_S) \in \Sigma^4$



Tile set: $T \subseteq \Sigma^4$

Adjacency rules:



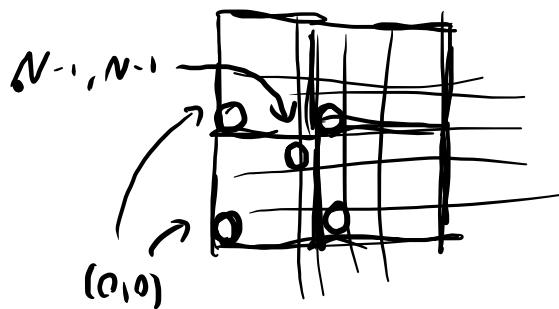
Tiling over T :  $\dots = x \in X_T \subseteq T^{\mathbb{Z}^2}$

Period of x : vector

$$\text{v s.t. } x_{w+v} = x_w \quad \forall w$$

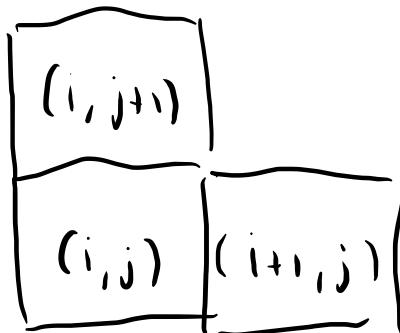
Shift space (of finite type)
or subshift

Fixed point const. for tile sets



How to simulate any given tile set S :

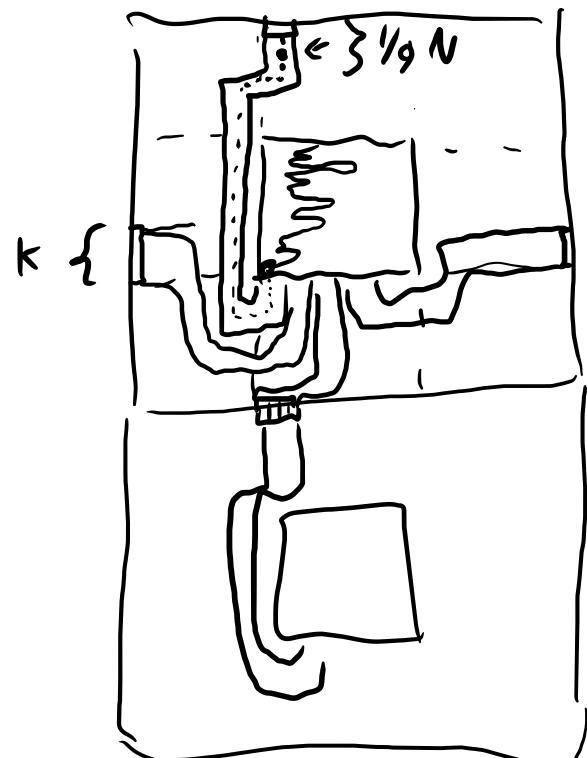
- Tiles of T have address $\in [0, N-1]^2$



$$\mod N$$

Role of tile depends
on its address!

Computation zone: addresses $\left[\frac{N}{3}, \frac{2N}{3}\right]^2$,
simulate Turing machine M_S



Border bits: encode colors of S , route to M_S , it checks they form a tile of S

\Rightarrow Simulation for large enough N

Want: $T = S$

Encode colors of T as bit vectors of length
 $\underbrace{2 \log N + m}_{\text{address}} \quad , \quad \underbrace{\text{other stuff (constant)}}_{m}, \quad \text{replace } M_s \text{ by a universal TM Mu and give it}$
a program $\underline{P_T} \in \{0,1\}^*$ on a separate tape.

length $\log N + m'$
 m'
constant

P_T is hard-coded: address $\frac{N}{3} + i$ has bit (p_T) ;



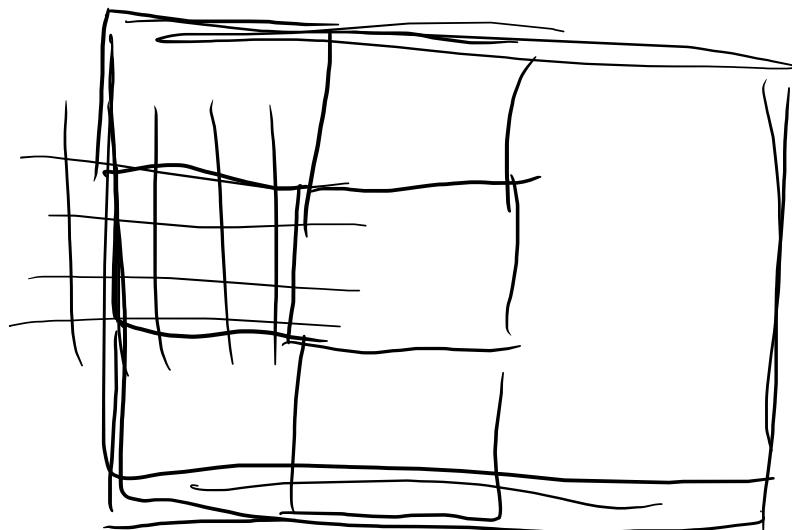
M_u can enforce this in
the simulated tiles, since it can read P_T

Since length of encoding of T is $2 \log N + m \ll N$

and $|P_T| = \log N + m' \ll N$

and time and space req. of $Mu = \text{poly}(\log N) \ll N$,

for large N , T simulates itself.



Variable zoom factor;
a macrotile can know
its level and simulate
tiling defined by

$P_T(L)$ computable + rom
 $P_T(L-1)$

Fixed point CA:

CA on $A^{\mathbb{Z}}$ has radius $r \geq 0$ and local rule
rule $F: A^{2r+1} \rightarrow A$, global rule $f: A^{\mathbb{Z}} \rightarrow A^{\mathbb{Z}}$

